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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10068148	FILING DATE 02/06/2002	CLASS 365	SUBCLASS	GAU 2818	EXAMINER A. J. GOV
**APPLICANTS: Lee Jae Goo.					
**CONTINUING DATA VERIFIED: NONE. <i>jk</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>jk</i> REPUBLIC OF KOREA 2001-7276 02/14/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>jk</i>				ATTORNEY DOCKET NO 5649-952	
TITLE : Integrated circuit memory devices with per-bit redundancy and methods of operation thereof					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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